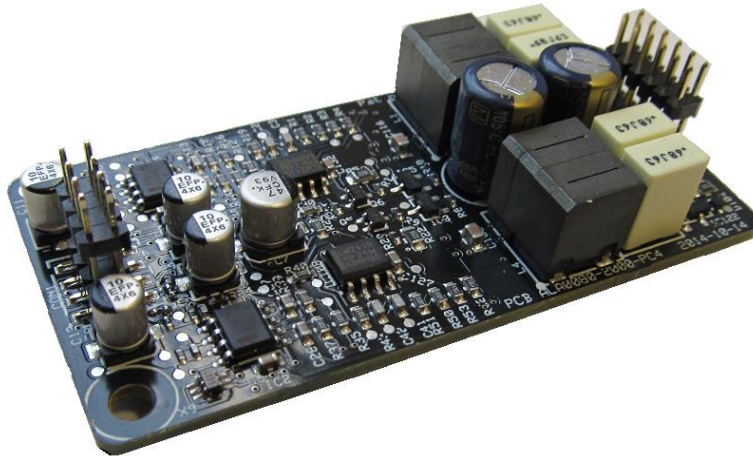


## PRODUCT SPECIFICATION AUDIO LINE AMPLIFIER ALA0080-2000



### FEATURE LIST

- 2x45Wrms into 6 $\Omega$  @ 1% THD
- Entirely differential patented APC (adaptive pole control) amplifier technology
- 80kHz load independent frequency range (-3dB)
- Very low THD in the audio band
- 115dB dynamic range
- Output impedance <math><5\text{m}\Omega</math> @ 100Hz
- Differential inputs with 0.1% resistors for improved CMRR

### SCOPE

These technical specifications describes the functionalities and features of the Anaview Audio Line Amplifier ALA0080-2000, capable of delivering up to 2x45W when supplied with a DC source between 12 and 25VDC. Typical applications are networked audio devices, portable audio devices, docking stations, audio receivers, powered speakers and residential audio system.

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## DISCLAIMER

The data sheet contains specifications that may be subject to change without prior notice. Responsibility for verifying the performance, safety, reliability and compliance with legal standards of end products using this subassembly falls to the manufacturer of said end product.

ANAVIEW products are not authorized for use as critical components in life support devices or life support systems without the express written approval of the president of ETAL Group AB. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labelling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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## ELECTRICAL SPECIFICATIONS

### ***Input specifications:***

<b>Supply voltage</b>	VS+: CON2:5, CON2:7 GND: CON2:6, CON2:8 Min: +9VDC(*1) Max: +30VDC Recommended: +12VDC to +25VDC	
<b>nENABLE</b>	Shut down input: CON1:5 "nENABLE" Shut down by: Leave floating Normal operation : Pull down to GND	
<b>IN+</b>	0 – 1.45 Vrms max Balanced audio input, left channel	
<b>IN-</b>	0 – 1.45 Vrms max Balanced audio input, right channel	
<b>Input impedance</b>	<b><u>Single ended input signal</u></b> +IN_CH1: CON1:8 Signal -IN_CH1: CON1:7 Ground Input impedance = 2.4kΩ  +IN_CH2: CON1:2 Signal -IN_CH2: CON1:1 Ground Input impedance = 2.4kΩ	<b><u>Balanced input signal</u></b> +IN_CH1: CON1:8 Signal -IN_CH1: CON1:7 Signal Input impedance = 2.3kΩ  +IN_CH2: CON1:2 Signal -IN_CH2: CON1:1 Signal Input impedance = 2.3kΩ

(\*1) ALA0080-2000 need a supply voltage of minimum 11.5VDC to start but won't shut down until the supply voltage drops below 9VDC.

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### Output specifications @ 12V:

Audio outputs	Typ. cont. output power (*1)	Max output power (*2)	THD
+OUT_CH1: CON2:1,3 -OUT_CH1: CON2:2,4	2x14W 4Ω 2x10W 6Ω 2x8W 8Ω	2x15W 4Ω 2x11W 6Ω 2x9W 8Ω	1%
+OUT_CH2: CON2:9,11 -OUT_CH2: CON2:10,12	2x14W 4Ω 2x10W 6Ω 2x8W 8Ω	2x15W 4Ω 2x11W 6Ω 2x9W 8Ω	1%

### Output specifications @ 20V:

Audio outputs	Typ. cont. output power (*1)	Max output power (*2)	THD
+OUT_CH1: CON2:1,3 -OUT_CH1: CON2:2,4	2x9W 4Ω 2x16W 6Ω 2x20W 8Ω	2x40W 4Ω 2x31W 6Ω 2x23W 8Ω	1%
+OUT_CH2: CON2:9,11 -OUT_CH2: CON2:10,12	2x9W 4Ω 2x16W 6Ω 2x20W 8Ω	2x40W 4Ω 2x31W 6Ω 2x23W 8Ω	1%

### Output specifications @ 25V:

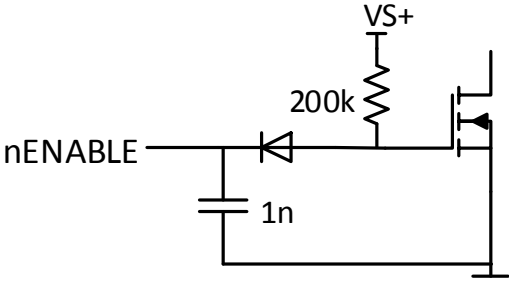
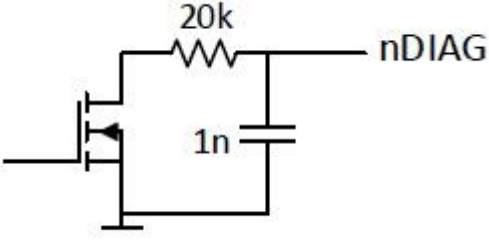
Audio outputs	Typ. cont. output power (*1)	Max output power (*2)	THD
+OUT_CH1: CON2:1,3 -OUT_CH1: CON2:2,4	2x11W 6Ω 2x13W 8Ω	2x45W 6Ω 2x35W 8Ω	1%
+OUT_CH2: CON2:9,11 -OUT_CH2: CON2:10,12	2x11W 6Ω 2x13W 8Ω	2x45W 6Ω 2x35W 8Ω	1%

(\*1) Typical continuous output power with an ambient temperature of 50°C.

(\*2) Maximum output power it the power delivered in the specified load with a maximum of 1% THD

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**Protections and functions:**

<p><b>Over temperature protection</b></p>	<p>Power shut down by over temperature. Outputs float when heat sink temperature reaches <math>&gt;110^{\circ}\text{C}</math>. Output switching resumes about 1s after temperature drops below the threshold.</p>		
<p><b>Under voltage protection</b></p>	<p>Power shut down by under voltage on VS+ (supply voltage).</p>		
	<p>Typical threshold</p>	<p>Shutdown</p>	<p>Restart</p>
<p><b>Over current protection</b></p>	<p>The over current protection is DC input voltage and temperature dependent but will allow full power <math>4\Omega</math> in <math>50^{\circ}\text{C}</math> ambient without over current protection. The over current protection will protect the amp outputs from short between them and short to DC input.</p>		
	<p><b>It will not protect against short to GND.</b></p>		
<p><b>nENABLE</b></p>	<p>Shut down input: CON1:5 "nENABLE"                  Shut down by: Leave pin floating                  Normal operation : Pull down to GND</p> 		
<p><b>nDIAG</b></p>	<p>Status output: CON1:3 "nDIAG"                  nDIAG goes low during under voltage and over temperature</p> 		

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<b>Offset voltage</b> (open inputs)	<5mV typ. (20mV max)
<b>Switching frequency</b> (idle)	300kHz to 500kHz
<b>Switching residual</b>	500mVrms typ.
<b>Minimum load impedance</b>	4Ω
<b>Recommended load</b>	4Ω to 8Ω with +20V supply voltage 6Ω to 8Ω with +25V supply voltage (*1)
<b>Gain</b> (f =1kHz)	22.7dB typ.
<b>Idle noise</b>	30 uVrms (unweighted) typ.
<b>Upper BW limit</b> (-3dB)	80kHz typ.
<b>Lower BW limit</b> (-3dB)	<5Hz
<b>Output impedance</b> (100Hz)	3mΩ typ. (5mΩ max)
<b>Residual noise vs freq</b>	See Figure 4
<b>THD vs PWR</b>	See Figure 5- 12
<b>THD vs freq</b>	See Figure 13 - 15
<b>Freq response</b>	See Figure 16
<b>Crosstalk</b>	See Figure 17

(\*1) ALA0080-2000 will not deliver specified full output power in 4Ω 50°C with +25V supply voltage without going into over current protection. Therefore it is not recommended to use 4Ω load with more than +20V supply voltage even if the amplifier will not take damage from it.

## GENERAL SPECIFICATIONS

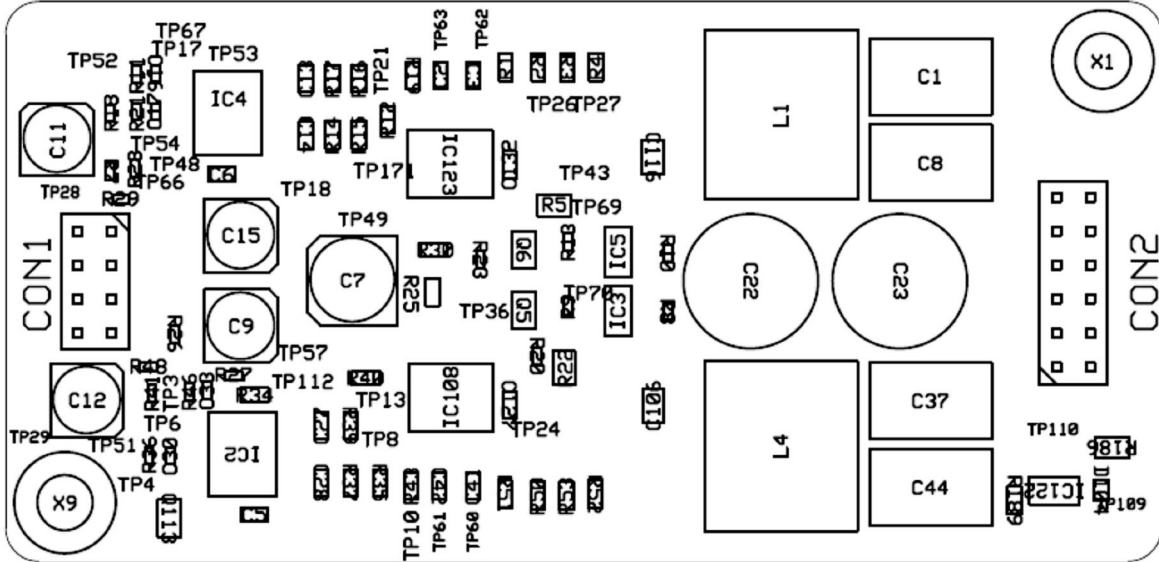
<b>Cooling</b>	Convection cooling
<b>Mounting of the unit</b>	See Figure 2. Board outline, dimensions & mounting holes. PCB may be secured to mating PCB using M3 size screws.
<b>Efficiency</b>	See Figure 18
<b>Idle current consumption</b>	60mA typ. With +12V supply voltage 75mA typ. With +20V supply voltage 85mA typ. With +25V supply voltage
<b>Disable current consumption</b>	35mA typ. With +12V supply voltage 47mA typ. With +20V supply voltage 55mA typ. With +25V supply voltage
<b>Manufacturing according to workmanship standard</b>	IPC-A-610, Revision D, February 2005

## ENVIRONMENTAL CONDITIONS

<b>Humidity</b>	5 – 85% RH non condensing
<b>Ambient Operating Temperature</b>	0°C to +50°C
<b>Storage Temperature</b>	-40°C to +85°C

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## CONNECTIONS



**Figure 1.** Bottom View, two dual row header connectors, CON1 and CON2

<b>Signal connector</b>	<p>CON1 : 8 pin (2.0mm) dual row vertical header Article number CVILux CB74082V100</p> <p><b>Mating connectors</b> CVILux CB74082V100 (Through hole) CVILux CB74082M100 (SMD)</p> <table border="0"> <thead> <tr> <th style="text-align: left;"><u>Pinning:</u></th> <th style="text-align: left;"><u>Description:</u></th> </tr> </thead> <tbody> <tr> <td>Pin 1 : -IN_CH2</td> <td>Negative input channel 2</td> </tr> <tr> <td>Pin 2 : +IN_CH2</td> <td>Positive input channel 2</td> </tr> <tr> <td>Pin 3 : nDIAG</td> <td>Status output</td> </tr> <tr> <td>Pin 4 : GND</td> <td>GND</td> </tr> <tr> <td>Pin 5 : nENABLE</td> <td>Enable input</td> </tr> <tr> <td>Pin 6 : GND</td> <td>GND</td> </tr> <tr> <td>Pin 7 : -IN_CH1</td> <td>Negative input channel 1</td> </tr> <tr> <td>Pin 8 : +IN_CH1</td> <td>Positive input channel 1</td> </tr> </tbody> </table>	<u>Pinning:</u>	<u>Description:</u>	Pin 1 : -IN_CH2	Negative input channel 2	Pin 2 : +IN_CH2	Positive input channel 2	Pin 3 : nDIAG	Status output	Pin 4 : GND	GND	Pin 5 : nENABLE	Enable input	Pin 6 : GND	GND	Pin 7 : -IN_CH1	Negative input channel 1	Pin 8 : +IN_CH1	Positive input channel 1
<u>Pinning:</u>	<u>Description:</u>																		
Pin 1 : -IN_CH2	Negative input channel 2																		
Pin 2 : +IN_CH2	Positive input channel 2																		
Pin 3 : nDIAG	Status output																		
Pin 4 : GND	GND																		
Pin 5 : nENABLE	Enable input																		
Pin 6 : GND	GND																		
Pin 7 : -IN_CH1	Negative input channel 1																		
Pin 8 : +IN_CH1	Positive input channel 1																		

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<b>Power connector</b>	CON2 : 12 pin (2.0mm) dual row vertical header Article number CVILux CB74122V100																									
	<p><b>Mating connectors</b> CVILux CB74122V100 (Through hole) CVILux CB74122M100 (SMD)</p> <table border="0"> <tr> <td><u>Pinning:</u></td> <td><u>Description:</u></td> </tr> <tr> <td>Pin 1 : +OUT_CH1</td> <td>Positive output</td> </tr> <tr> <td>Pin 2 : -OUT_CH1</td> <td>Negative output</td> </tr> <tr> <td>Pin 3 : +OUT_CH1</td> <td>Positive output</td> </tr> <tr> <td>Pin 4 : -OUT_CH1</td> <td>Negative output</td> </tr> <tr> <td>Pin 5 : VS+</td> <td>Positive supply voltage</td> </tr> <tr> <td>Pin 6 : GND</td> <td>GND</td> </tr> <tr> <td>Pin 7 : VS+</td> <td>Positive supply voltage</td> </tr> <tr> <td>Pin 8 : GND</td> <td>GND</td> </tr> <tr> <td>Pin 9 : +OUT_CH2</td> <td>Positive output</td> </tr> <tr> <td>Pin 10 : -OUT_CH2</td> <td>Negative output</td> </tr> <tr> <td>Pin 11 : +OUT_CH2</td> <td>Positive output</td> </tr> <tr> <td>Pin 12 : -OUT_CH2</td> <td>Negative output</td> </tr> </table>	<u>Pinning:</u>	<u>Description:</u>	Pin 1 : +OUT_CH1	Positive output	Pin 2 : -OUT_CH1	Negative output	Pin 3 : +OUT_CH1	Positive output	Pin 4 : -OUT_CH1	Negative output	Pin 5 : VS+	Positive supply voltage	Pin 6 : GND	GND	Pin 7 : VS+	Positive supply voltage	Pin 8 : GND	GND	Pin 9 : +OUT_CH2	Positive output	Pin 10 : -OUT_CH2	Negative output	Pin 11 : +OUT_CH2	Positive output	Pin 12 : -OUT_CH2
<u>Pinning:</u>	<u>Description:</u>																									
Pin 1 : +OUT_CH1	Positive output																									
Pin 2 : -OUT_CH1	Negative output																									
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Pin 4 : -OUT_CH1	Negative output																									
Pin 5 : VS+	Positive supply voltage																									
Pin 6 : GND	GND																									
Pin 7 : VS+	Positive supply voltage																									
Pin 8 : GND	GND																									
Pin 9 : +OUT_CH2	Positive output																									
Pin 10 : -OUT_CH2	Negative output																									
Pin 11 : +OUT_CH2	Positive output																									
Pin 12 : -OUT_CH2	Negative output																									

## REGULATIONS AND COMPLIANCES

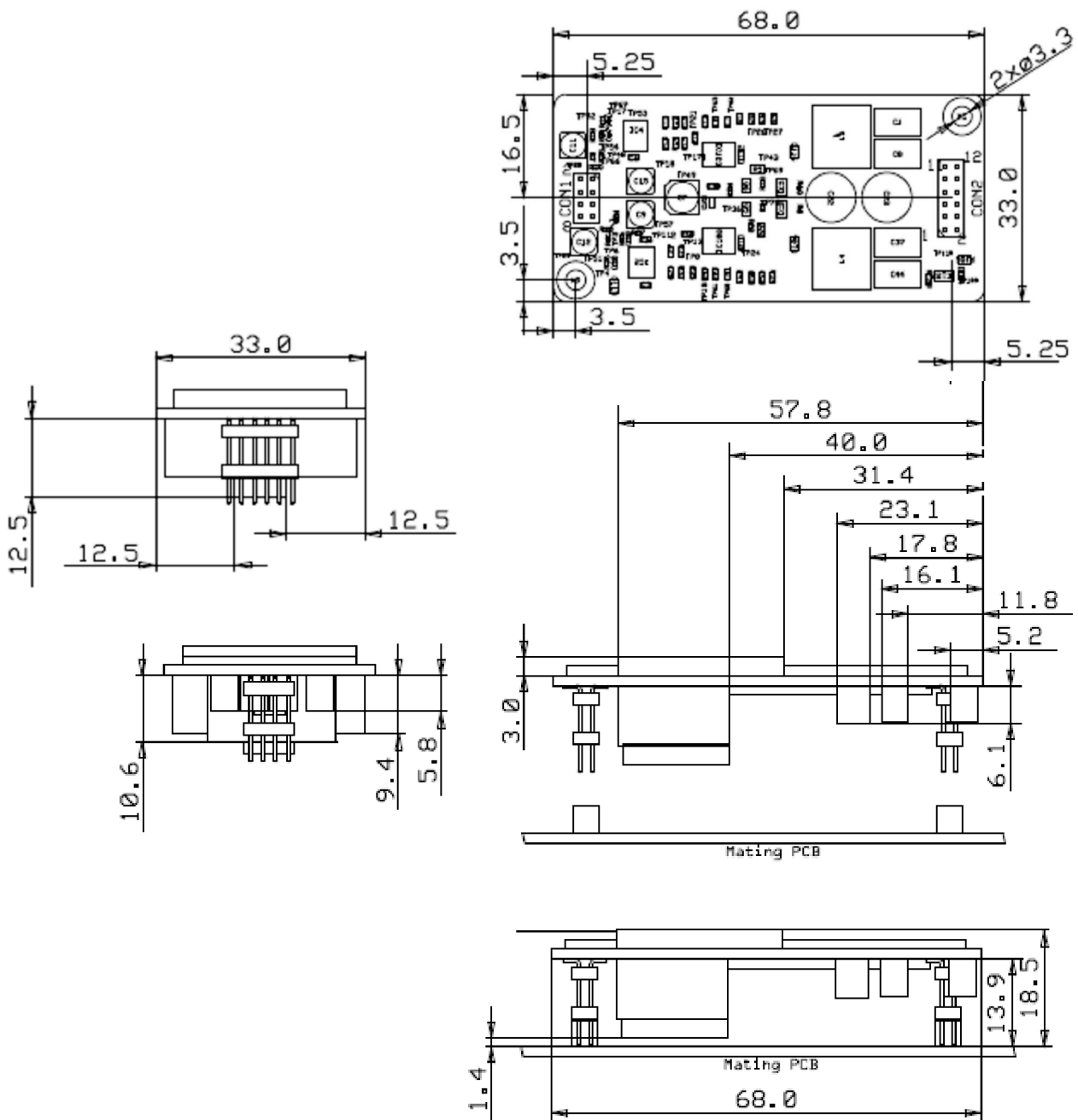
<b>EMC</b>	Emission	EN 55022:2006+A1:2007+A2:2006 Class "B" FCC part 15 (CISPR22) Class "B" Tested at a level of 1/8 of the max output power.
	Immunity	IEC 61000-4-2 (2008) IEC 61000-4-3 (2006) IEC 61000-4-4 (2004) *Only I/O line applicable IEC 61000-4-6 (2008) *Only I/O line applicable IEC 61000-4-8 (2009)

## MECHANICAL OUTLINE

<b>Size (l x w x h)</b>	68x33x18mm, see Figure 2. Board outline, dimensions below. Max component height/lead length on PCB top side: 4.0 mm Max component height/lead length on PCB bottom side: 13.3 mm Max total height including mating connector: 18.5mm
<b>Weight</b>	26g
<b>IP figures, encapsulation</b> IP XY (X=Solids, Y=Liquids)	Open frame
<b>Coloring, design and branding</b>	ALA0080-2000, black PCB

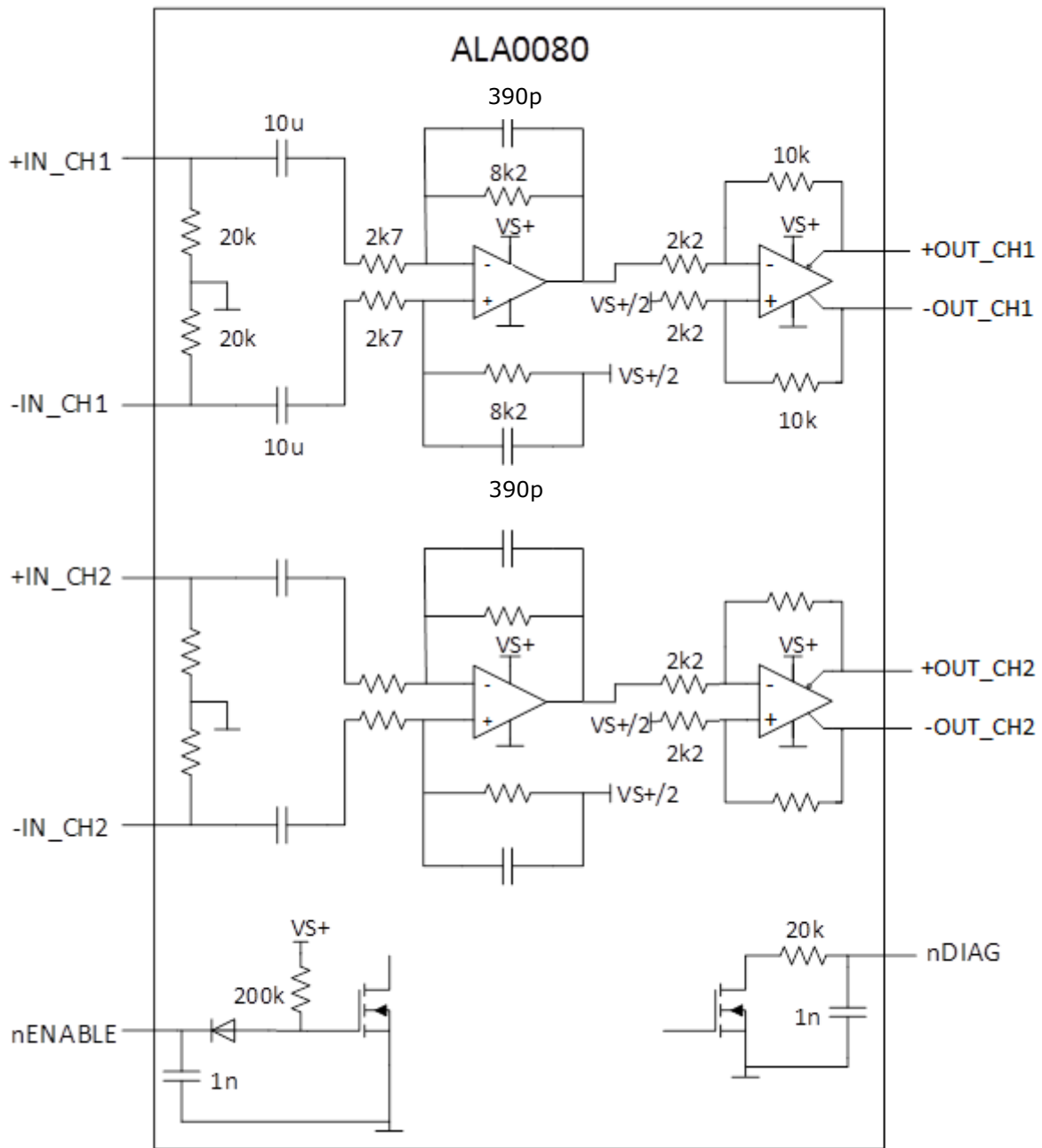
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**Figure 2.** Board outline, dimensions and side views

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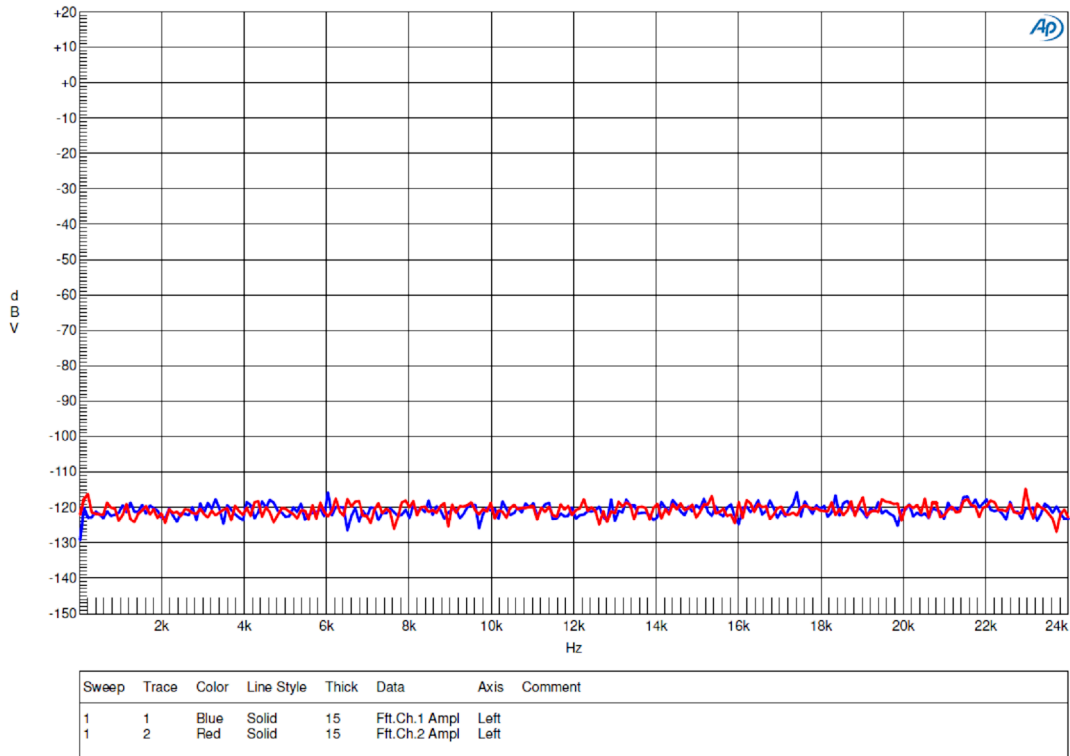


**Figure 3.** Connection diagram.

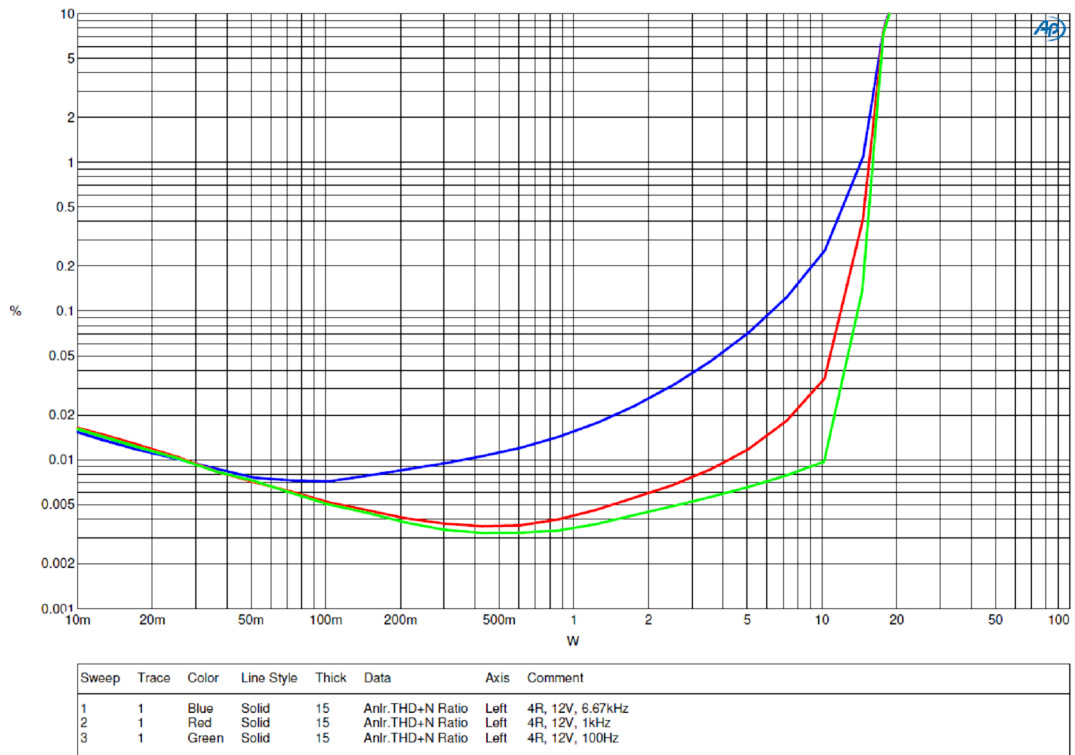
### LOAD CONSIDERATION

<b>Connecting different loads</b>	For optimum noise performance when connecting different speakers/loads on the amp channels, the load with highest impedance at 400kHz should be connected to output channel 2 (OUT_CH2).
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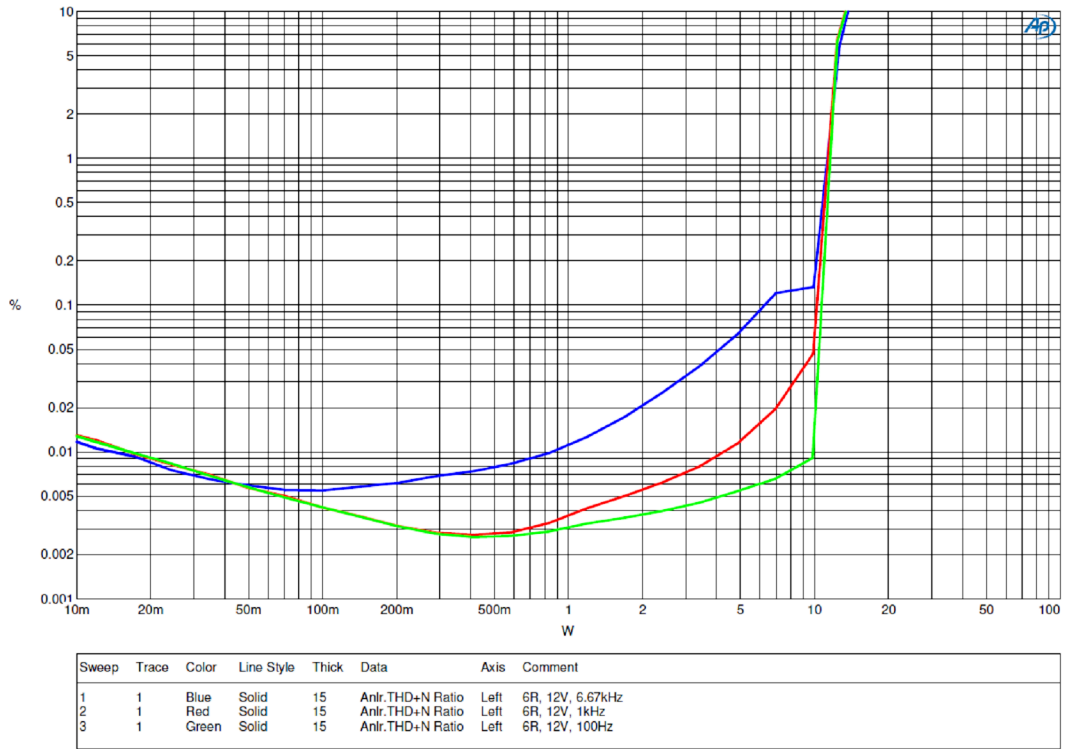


**Figure 4.** Residual noise.

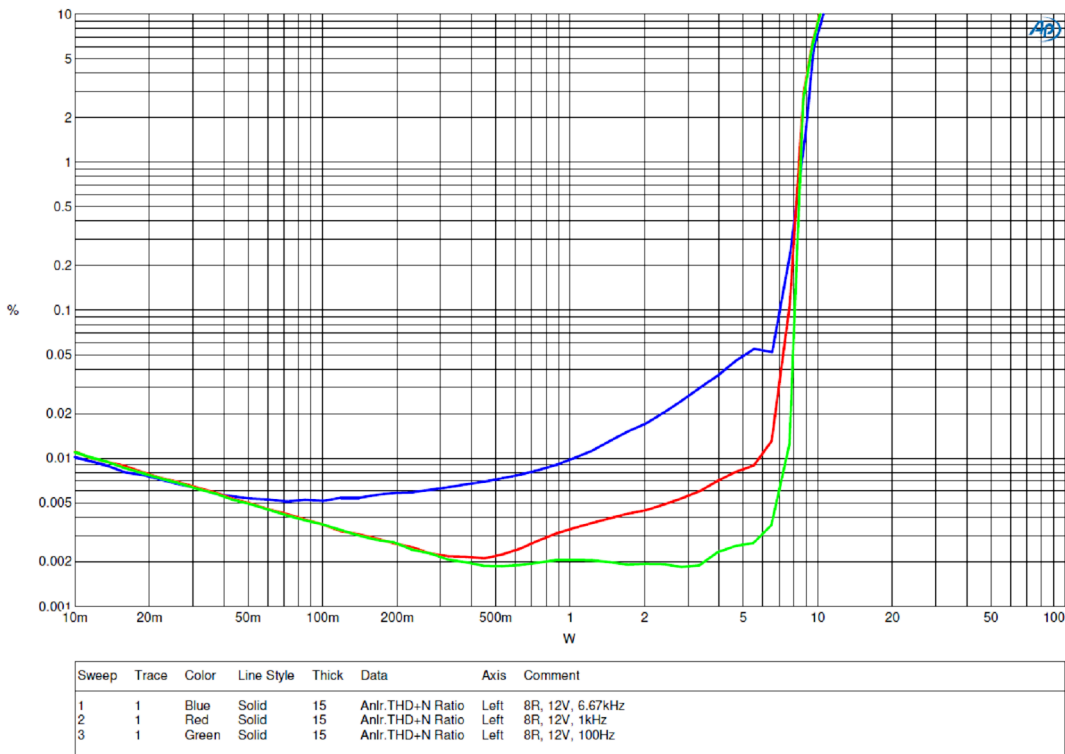


**Figure 5.** THD vs power, 4Ω +12V supply.

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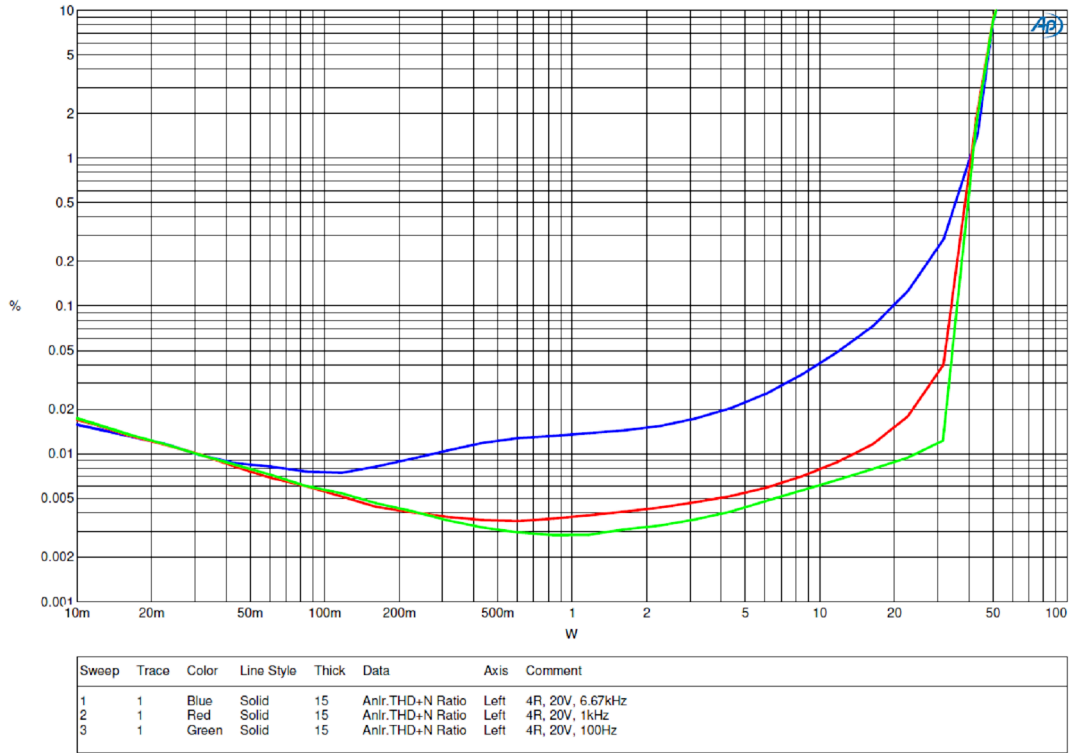


**Figure 6.** THD vs power, 6Ω +12V supply.

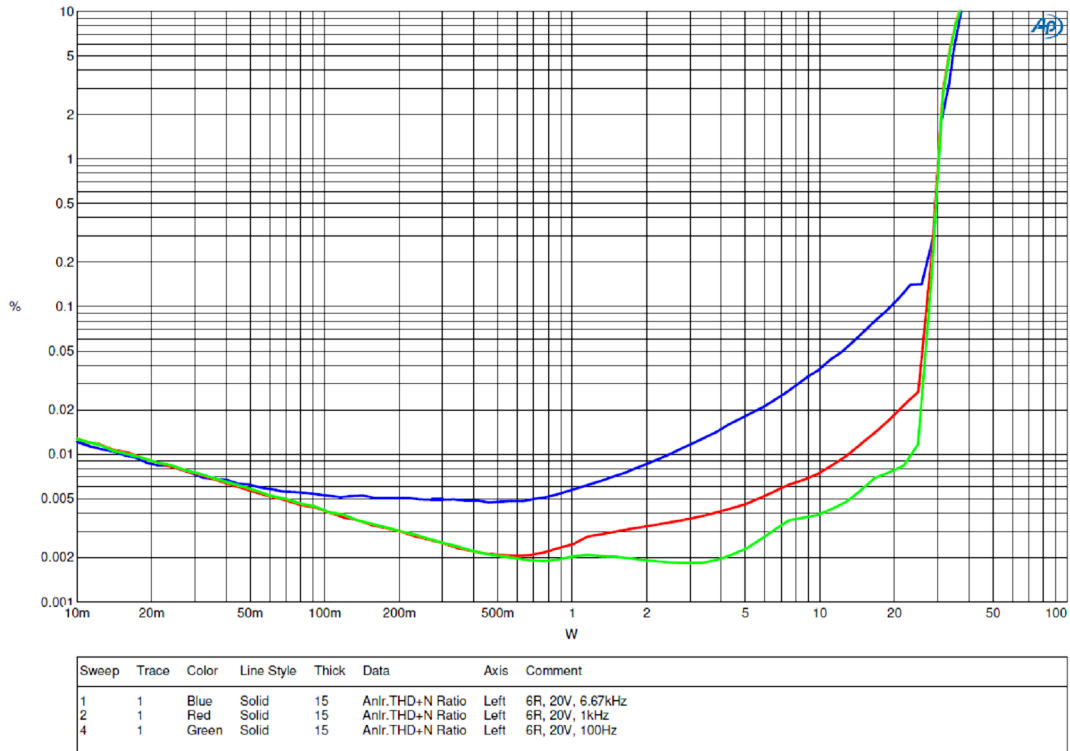


**Figure 7.** THD vs power, 8Ω +12V supply.

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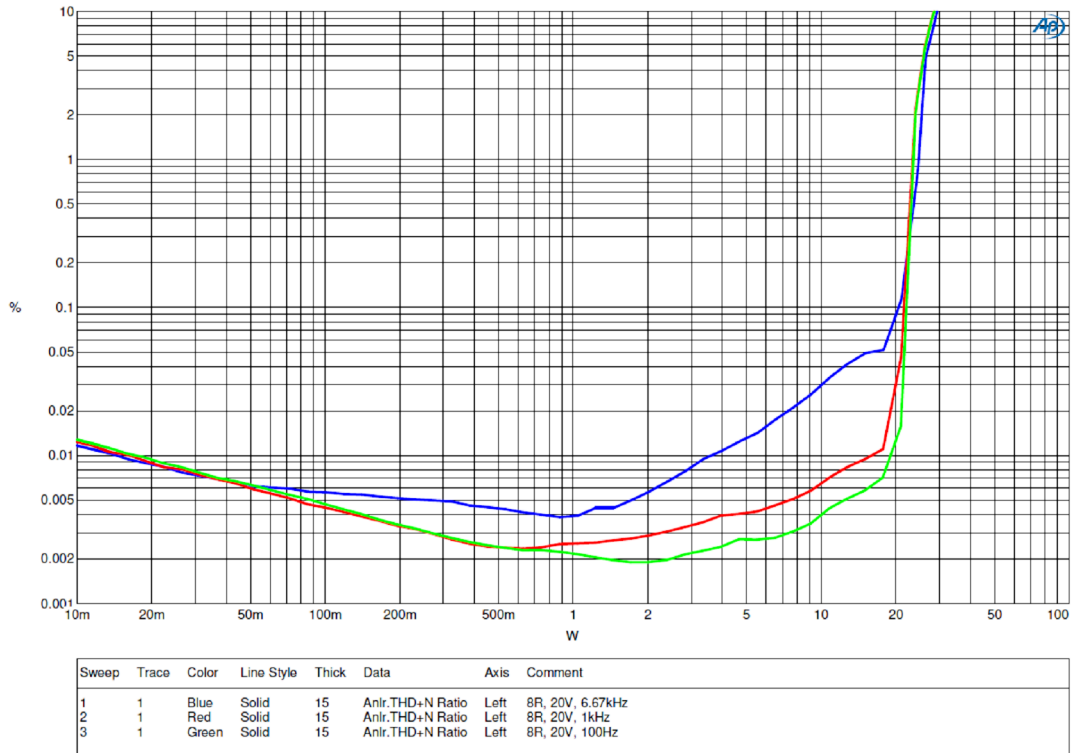


**Figure 8.** THD vs power, 4Ω +20V supply.

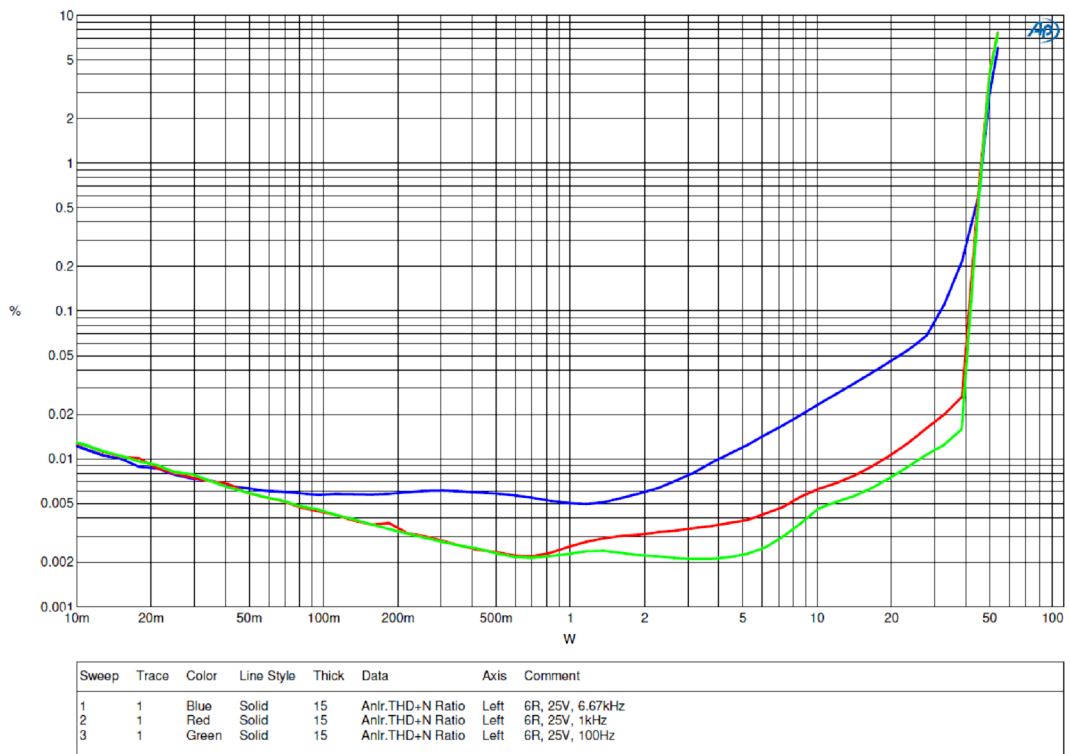


**Figure 9.** THD vs power, 6Ω +20V supply.

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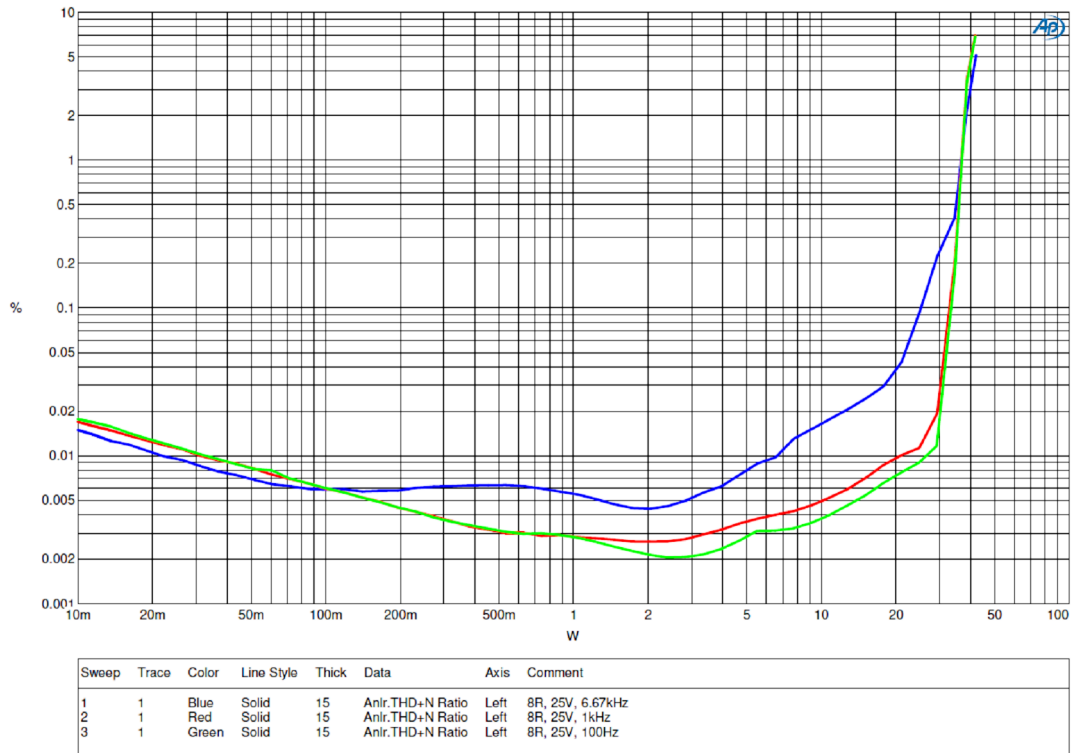


**Figure 10.** THD vs power, 8Ω +20V supply.

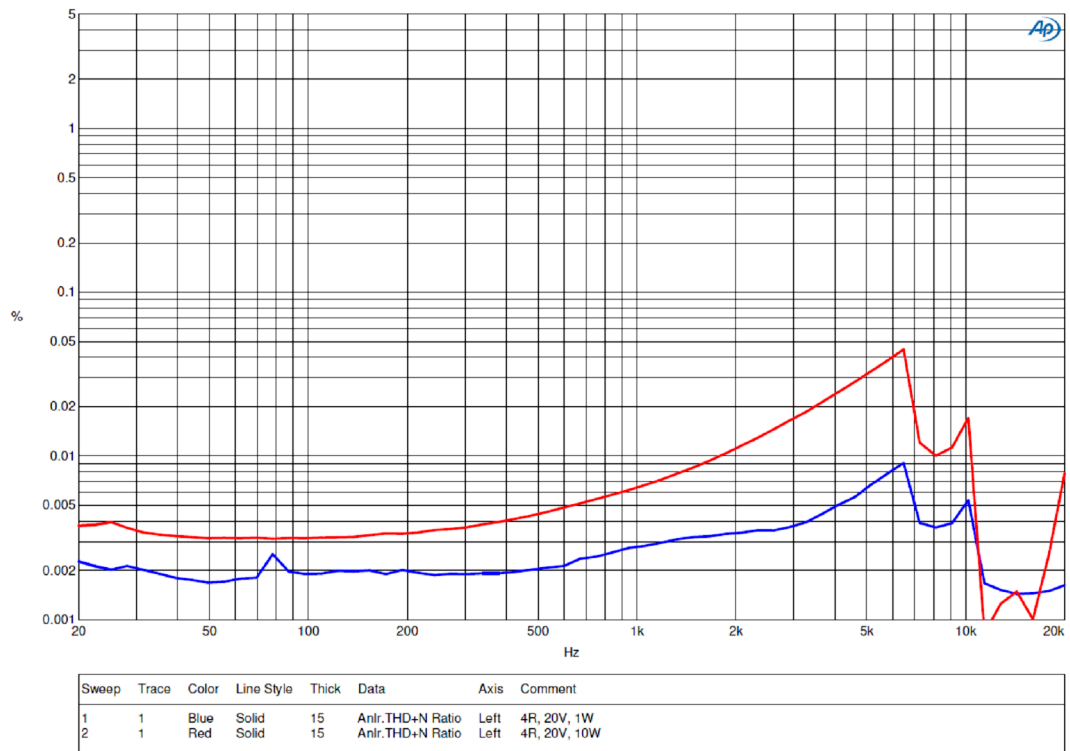


**Figure 11.** THD vs power, 6Ω +25V supply.

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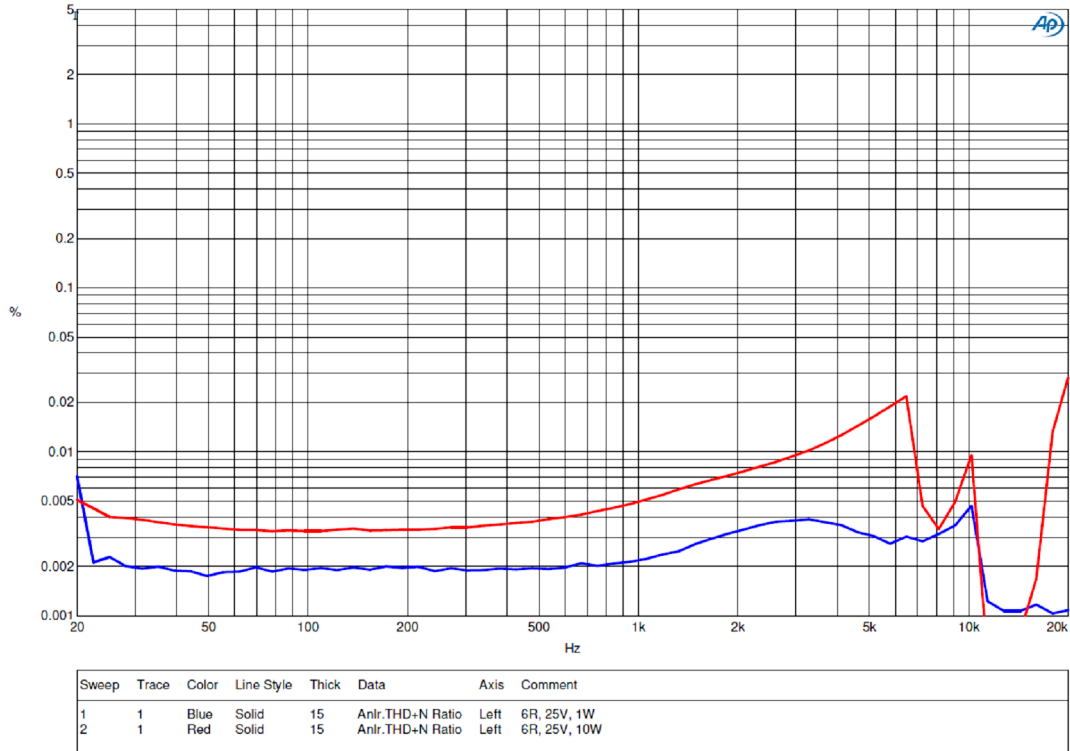


**Figure 12.** THD vs power, 8Ω +25V supply.

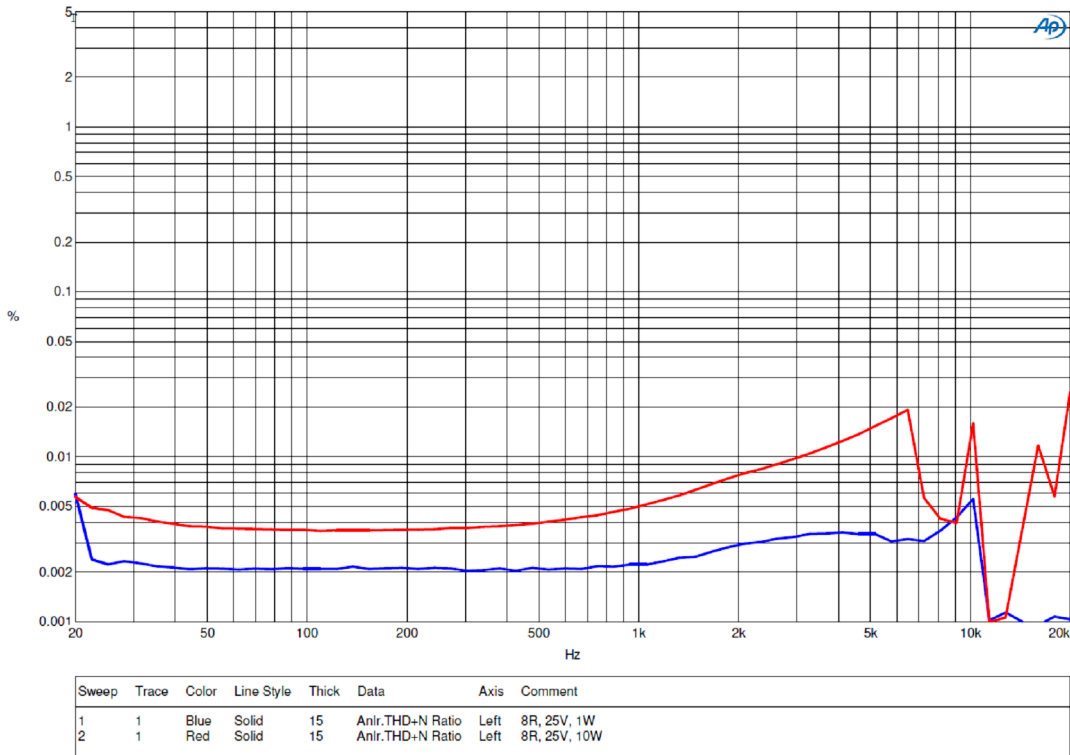


**Figure 13.** THD+N vs frequency, 4Ω +20V supply.

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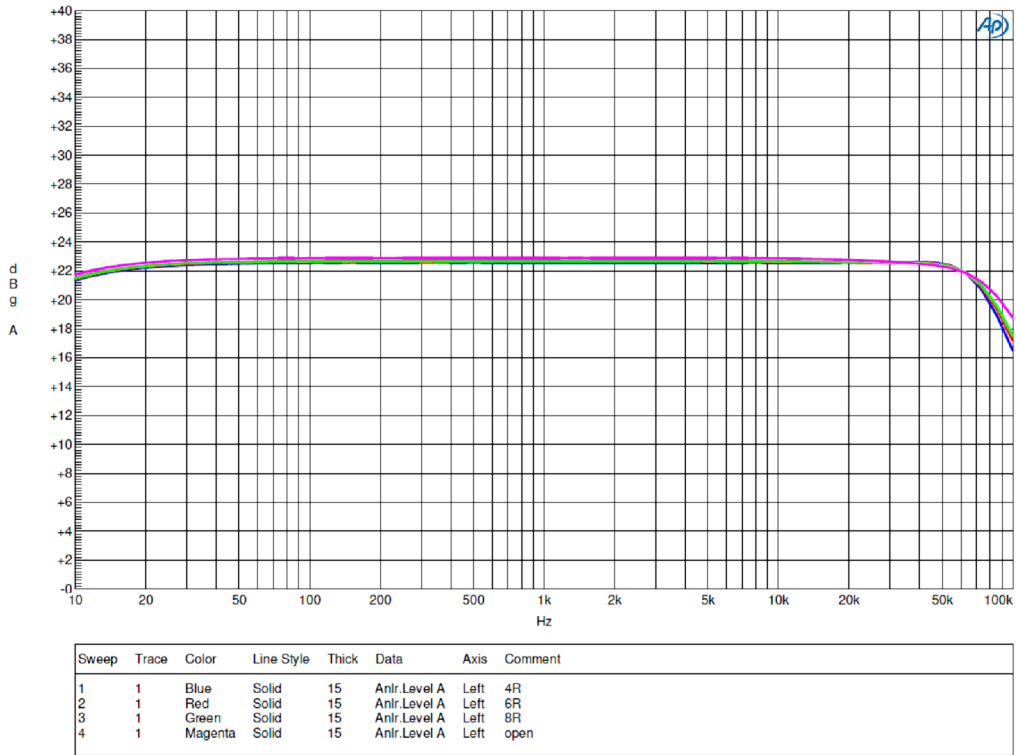
**Figure 14.** THD+N vs frequency, 6Ω +25V supply.



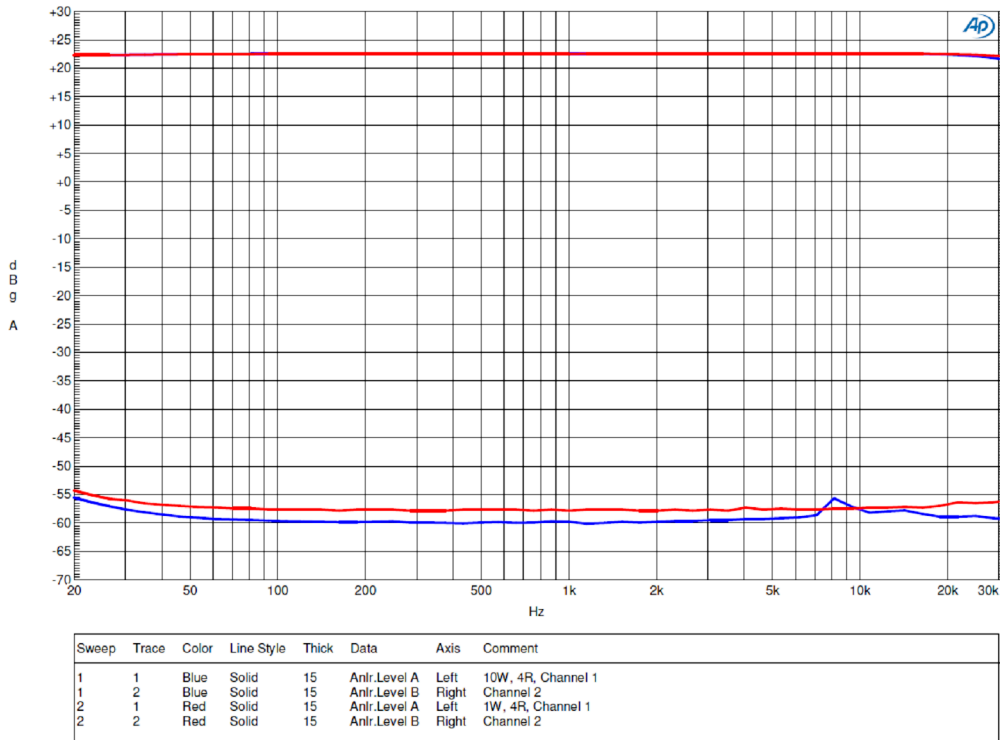
**Figure 15.** THD+N vs frequency, 8Ω +25V supply.

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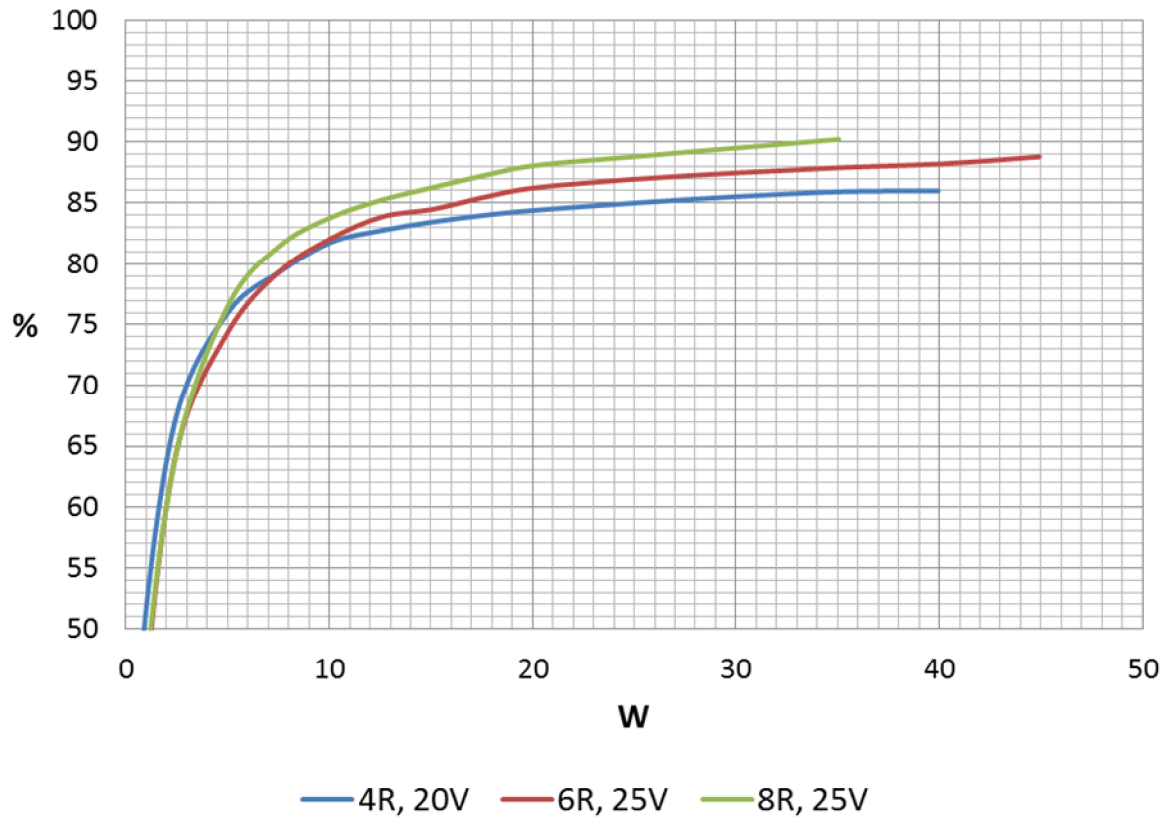


**Figure 16.** Frequency responses 4ohms, 6ohms, 8ohms, open circuit.



**Figure 17.** Inter channel crosstalk

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**Figure 18.** Typical efficiency, both channels powered.

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## REVISION LOG

Rev.	Date	Item	Sign
PA1	2015-02-11	Preliminary release revision	JN
A	2015-06-02	Official release	RK

## ANAVIEW CONTACT INFORMATION

For further information about Anaview's products and technology please contact:

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Website: [www.anaview.com](http://www.anaview.com)

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SWEDEN

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